

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:  
Jonathan Meigs, et al.

Docket: 30-4590 (4950)

Serial Number: 09/361,458

Group Art Unit: 2832

Filed: July 27, 1999

Examiner: Karl Easthom

For: COMPOSITION AND METHOD FOR MANUFACTURING INTEGRAL  
RESISTORS IN PRINTED CIRCUIT BOARDS

NOTICE: THIS APPLICATION IS UNDER FINAL REJECTIONFAX COVER SHEET

TO: Commissioner for Patents  
Washington, D.C. 20231

FAX NO.: (703) 703-308-7722

FROM: Richard S. Roberts  
Reg. No. 27941  
P.O. Box 484  
Princeton, New Jersey 08542  
(609) 921-3500

DATE: February 24, 2003

KINDLY DIRECT THIS COMMUNICATION TO:

EXAMINER :Karl D. Eastom  
GROUP : 2832

NO. OF PAGES SENT INCLUDING THIS COVER SHEET: 7

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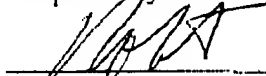
Commissioner for Patents  
Washington, D.C. 20231**TECHNOLOGY CENTER 2800**

Sir:

No extension of time fee is required by this submission. The office action was mailed November 22, 2002. Therefore a response is due by February 22, 2003. Since February 22, 2003 was a Saturday, this response is timely filed on Monday February 24, 2003.

In the event that the Commissioner determines that an extension of time is required in order for this submission to be timely, it is requested that this submission include a petition for an extension for the required length of time and the Commissioner is authorized to charge any fees necessitated by this paper to Deposit Acct. 18-1589.

Respectfully submitted,



Richard S. Roberts  
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Date: February 24, 2003